

PRODUCT SPECIFICATION

6.2" Mono TFT LCD MODULE

MODEL: T062640320-A0WMN-001 Ver:1.4



< ◇ > Preliminary Specification

< ◆ > Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Revision History

Revision	Date	Originator	Detail	Remarks
1.0	2013.04.19		Initial Release	
1.1	2013.05.11		Modify PIN1 assignment	P9,P10,P26
1.2	2013.08.21		Modify Resolution	P4
1.3	2014.04.28		Add weight Add current Modify Chromaticity Transmissive Modify Inspection Specification Modify Reliability Specification Modify Outline Drawing	P4 P5 P6 P17 P22 P26
1.4	2018.04.26	ZFY	Add LED working life Modify many details	P5 P23/P24

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1. General Description

The specification is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver Ics and a backlight unit.

2. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	6.2"	
Display Mode	Transmissive / Normally Black VA	
Resolution	640 x 320	Pixels
View Direction	WIDE VIEW	Best Image
Module Outline	151.3(H) x 88.3(V) x 2.8(T) (Note1)	mm
Active Area	140 (H) x70(V)	mm
Pixel Pitch	218.75(H) x 218.75(V)	um
Pixel Arrangement	Mono Stripe	
Interface	8-bit 8080/8-bit 6800/3-line/4-line Interface	
Driver IC	ST7511	-
With or Without Touch Panel	Without	
Operating Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	82	g

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

3. Absolute Maximum Ratings

$V_{SS}=0V, T_a=25^{\circ}C$

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VDDA	2.7	5.5	V
	VDDI	2.7	5.5	V
Storage temperature	T _{stg}	-30	+80	°C
Operating temperature	T _{op}	-20	+70	°C

Note 1: If T_a below $50^{\circ}C$, the maximal humidity is 90%RH, if T_a over $50^{\circ}C$, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around $-10^{\circ}C$, and the back ground will become darker at high temperature operating.

4. DC Characteristics

 $V_{SS}=0V, T_a=25^{\circ}C$

Item	Symbol	Min.	Typ.	Max.	Unit	
Supply Voltage	VDDA	3.0	3.3	5.0	V	
	VDDI	3.0	3.3	5.0	V	
Logic Low input voltage	V_{IL}	GND	-	$0.2 \cdot VDDI$	V	
Logic High input voltage	V_{IH}	$0.8 \cdot VDDI$	-	VDDI	V	
Logic Low output voltage	V_{OL}	GND	-	$0.2 \cdot VDDI$	V	
Logic High output voltage	V_{OH}	$0.8 \cdot VDDI$	-	VDDI	V	
Current Consumption All White	Logic Analog	$I_{CC+I_{IN}}$	-	(30)	(50)	mA

5. Backlight Characteristic

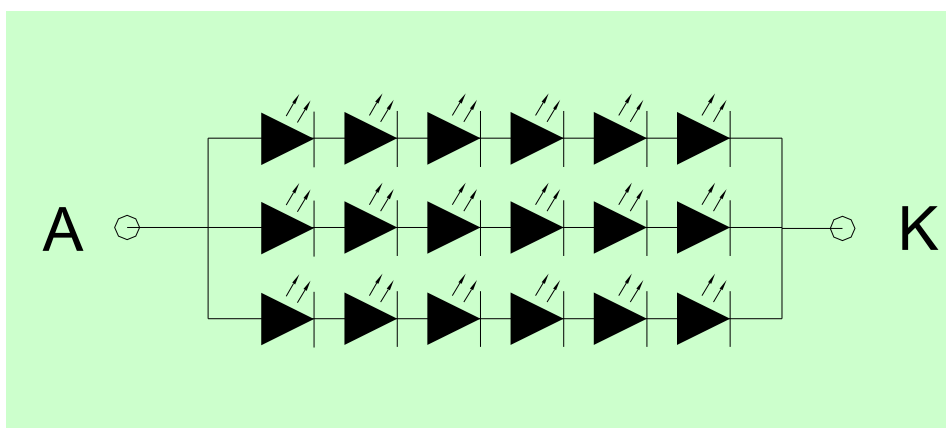
5.1. Backlight Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V_F	$T_a=25^{\circ}C, I_F=20mA/LED$	17.4	19.2	19.8	V
Forward Current	I_F	$T_a=25^{\circ}C, V_F=3.2V/LED$	-	60	-	mA
Power dissipation	P_D	-	-	1152	-	W
Uniformity	Avg	-	-	80	-	%
LED working life($25^{\circ}C$)	-	-	-	20000	-	Hrs
Drive method	Constant current					
LED Configuration	18 White LEDs (6 LEDs in one string and 3 groups in parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.

The environmental conducted under ambient air flow, at $T_a=25 \pm 2^{\circ}C, 60\%RH \pm 5\%, I_F=20mA$

5.2. Backlighting circuit



6. Optical Characteristics

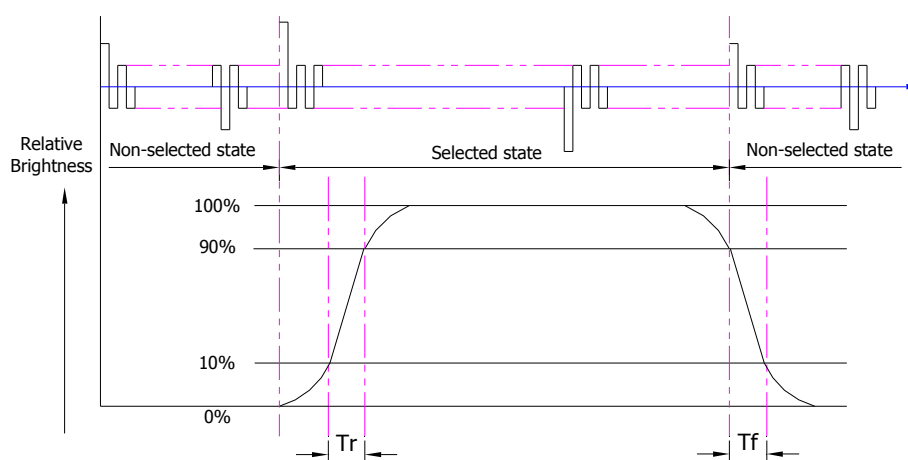
6.1. Optical Characteristics

Ta=25°C,

	Item	Symbol	Condition	Specification			Unit	
				Min.	Typ.	Max.		
Backlight On (Transmissive Mode)	Luminance on TFT($I_f=20\text{mA/LED}$)	Lv		480	600	-	cd/m ²	
	Contrast ratio(See 6.3)	CR		-	TBD	-		
	Response time (See 6.2)	TR		-	TBD		ms	
		TF						
	Chromaticity Transmissive (See 6.5)	Red	XR	Backlight is on		TBD		-
			YR			TBD		-
		Green	XG			TBD		-
			YG			TBD		-
		Blue	XB			TBD		-
			YB			TBD		-
	White	XW		0.234	0.284	0.334	-	
		YW		0.271	0.321	0.371	-	
	Viewing Angle (See 6.4)	Horizontal	θ_{x+}	Center CR \geq 10	70	80	-	Deg.
θ_{x-}			70		80	-		
Vertical		φ_{y+}	70		80	-		
		φ_{y-}	70		80	-		
NTSC Ratio(Gamut)	-	$\theta=0^\circ$	-	TBD	-	%		

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

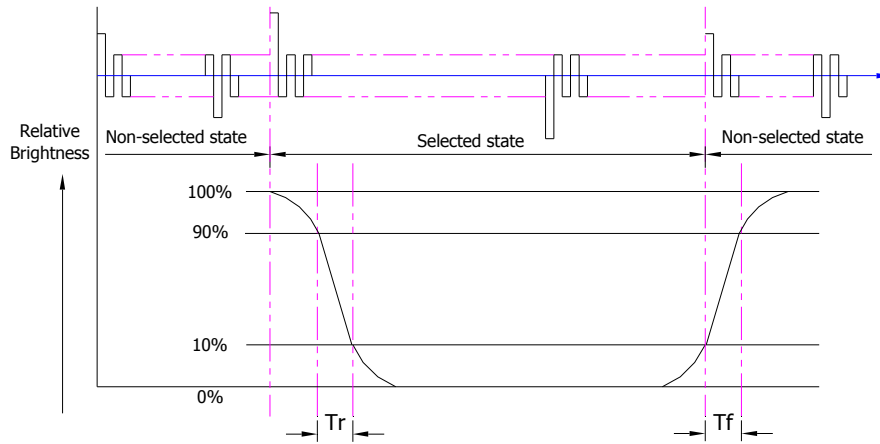


Tr is the time it takes to change from non-selected stage with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note : Measuring machine: LCD-5100 or EQUI

6.3. Definition of Contrast Ratio

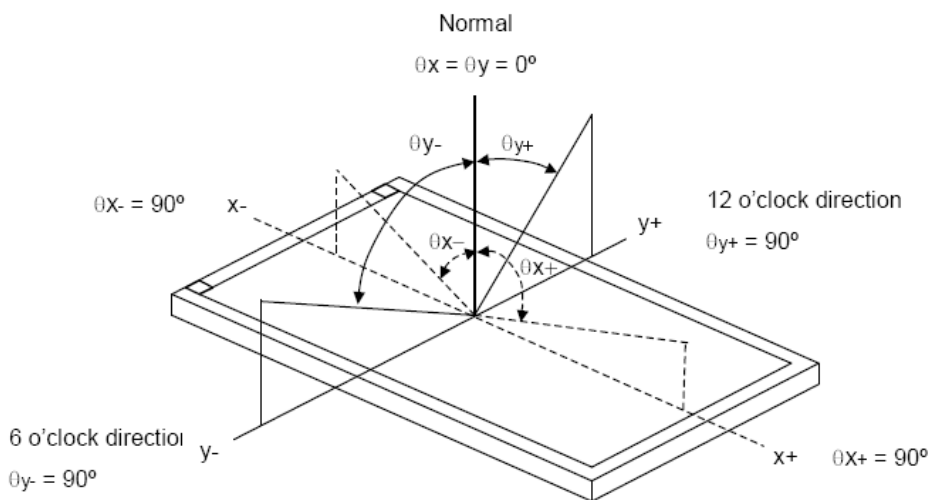
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles



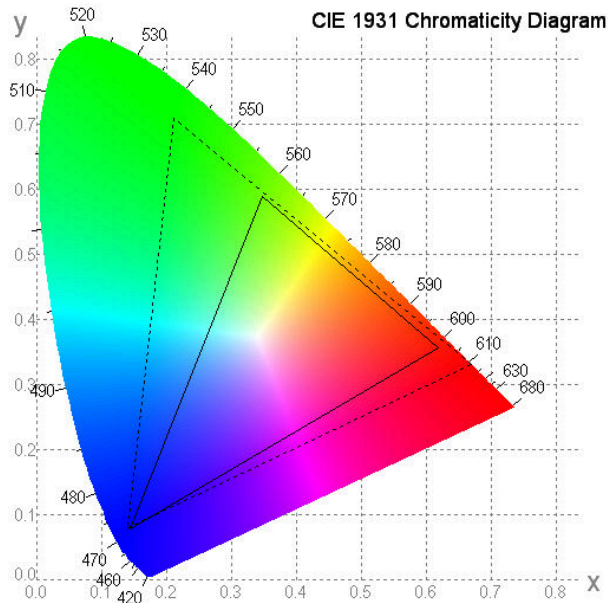
Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



6.6. Definition of Surface Luminance, Uniformity and Transmittance

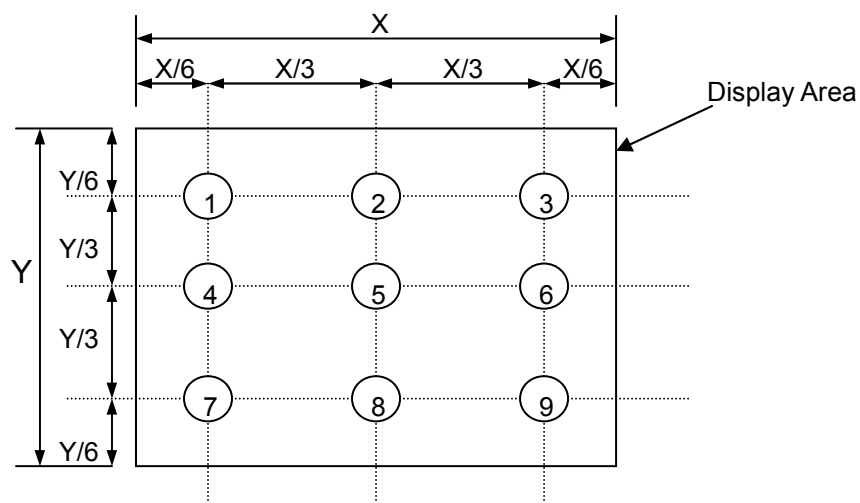
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

6.6.1. Surface Luminance: $L_V = \text{average} (L_{P1}:L_{P9})$

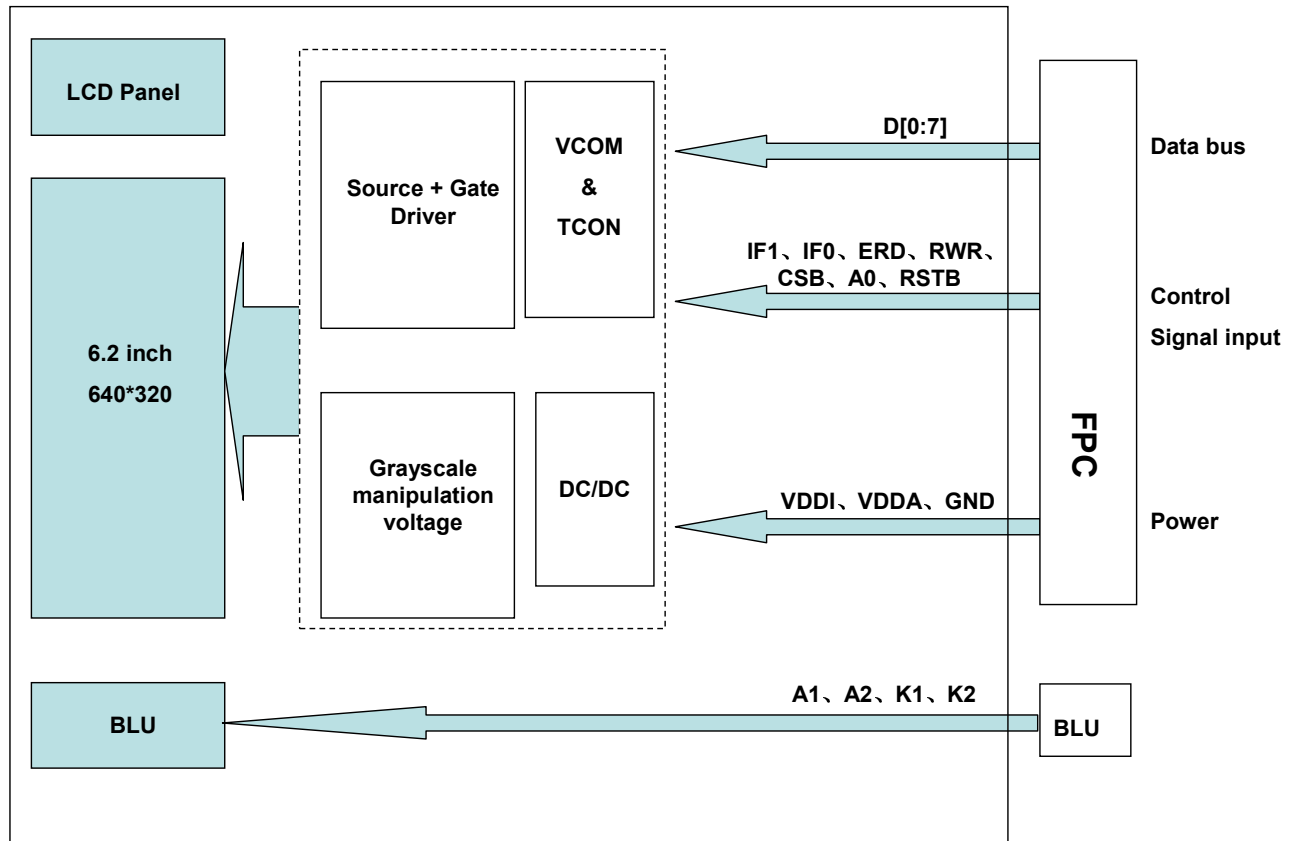
6.6.2. Uniformity = Minimal ($L_{P1}:L_{P9}$) / Maximal ($L_{P1}:L_{P9}$) * 100%

6.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note: Measuring machine: BM-7



7. Block Diagram and Power Supply



8. Interface Pins Definition

No.	Symbol	Function	Remark
1	GND	Ground	
2	GND	Ground	
3	VDDA	Power supply.	
4	VDDI	Power supply.	
5	D0	When using 8-bit parallel interface(6800 or 8080 mode): 8-bit bi-directional data bus.	
6	D1		
7	D2		
8	D3	When using serial interface:3-line or 4-line: D7: Serial input clock (SCL). D0: Serial data (SDA). D[6:1]:fix to "H" by VDDI.	
9	D4		
10	D5		
11	D6		
12	D7		
13	NC	No connection	
14	NC	No connection	
15	NC	No connection	
16	NC	No connection	
17	NC	No connection	
18	NC	No connection	
19	NC	No connection	
20	NC	No connection	
21	NC	No connection	
22	NC	No connection	
23	NC	No connection	
24	NC	No connection	
25	NC	No connection	
26	NC	No connection	
27	NC	No connection	
28	NC	No connection	
29	GND	Ground	
30	NC	No connection	
31	NC	No connection	
32	GND	Ground	
33	NC	No connection	
34	NC	No connection	
35	RSTB	Reset input pin.	
36	NC	No connection	
37	A0	Data/Command identification pin	
38	CSB	Chip select input pin	

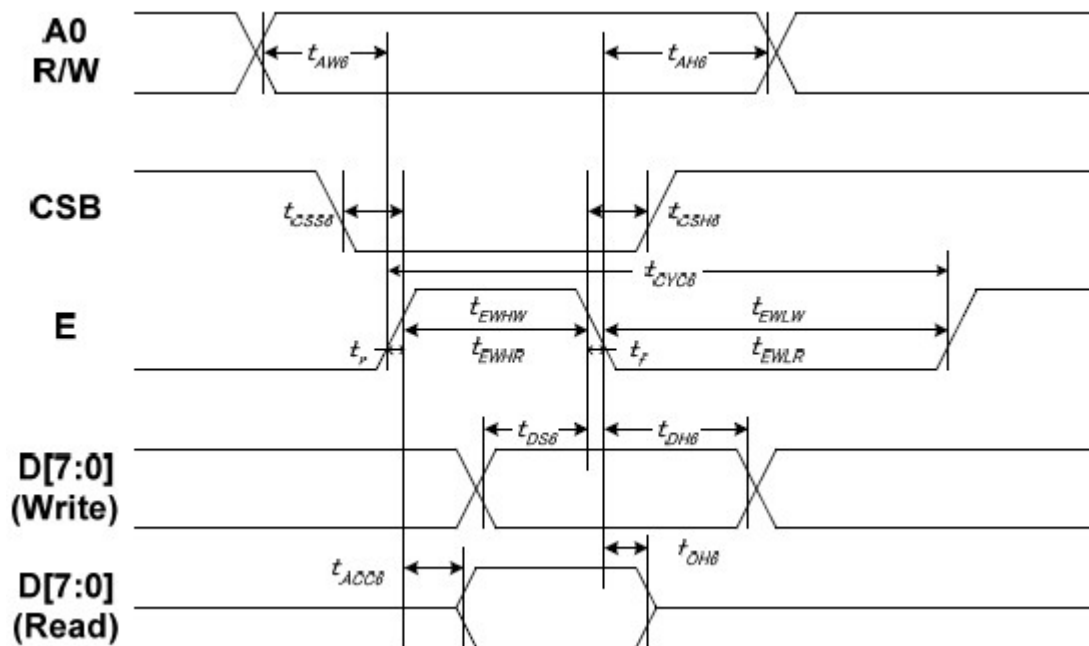
39	RWR	Read/Write execution control pin. When IF1 is "L".			
		IF0	MPU Type	ERD	Description
		H	6800 series	E	Read/Write control input pin.
		L	8080 series	/RD	Read enable input pin
40	ERD	Read/Write execution control pin. When IF1 is "L".			
		IF0	MPU Type	ERD	Description
		H	6800 series	R/W	Read/Write control input pin.
		L	8080 series	/WR	Write enable input pin
41	NC	No connection			
42	NC	No connection			
43	IF0	IF1	IF0	Selected Interface	
		L	L	8-bit 8080 parallel interface	
		L	H	8-bit 6800 parallel interface	
44	IF1	H	L	3-line serial interface	
		H	H	4-line serial interface	
45	NC	No connection			
46	NC	No connection			
47	GND	Ground			
48	GND	Ground			
49	NC	No connection			
50	NC	No connection			

PIN2 assignment

No.	Symbol	Function	Remark
1	A1	LED Anode.	
2	A2	LED Anode.	
3	K1	LED Cathode.	
4	K2	LED Cathode.	

9. AC Characteristics

9.1. System Bus Timing for 6800 Series MPU



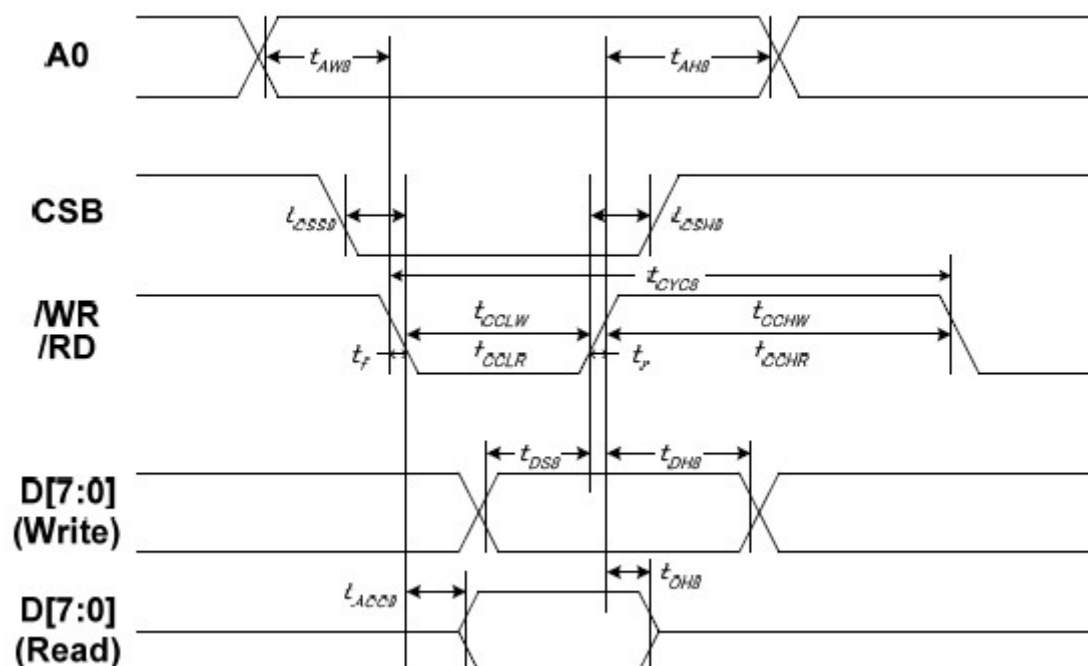
VDD1 = 3.3V~5.0V, Ta = 25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW6		10	—	ns
Address hold time		tAH6		0	—	
System cycle time	E	tCYC6		1100	—	
Enable L pulse width (WRITE)		tEHLW		500	—	
Enable H pulse width (WRITE)		tEHWL		500	—	
Enable L pulse width (READ)		tEHLR		500	—	
Enable H pulse width (READ)		tEHWL		500	—	
CSB setup time		CSB	tCSS6		100	
CSB hold time	tCSH6			100	—	
Write data setup time	D[7:0]	tDS6		200	—	
Write data hold time		tDH6		60	—	
Read data access time		tACC6	CL = 100 pF	—	950	
Read data output disable time		tOH6	CL = 100 pF	5	200	

Note:

- The input signal rise time and fall time (t_r , t_f) is specified at 15 ns or less. When the system cycle time is extremely fast, $(t_r + t_f) \leq (t_{CYC6} - t_{CCLW} - t_{CCHW})$ for $(t_r + t_f) \leq (t_{CYC6} - t_{CCLR} - t_{CCHR})$ are specified.
- All timing is specified using 20% and 80% of VDD1 as the reference.
- t_{CCLW} and t_{CCLR} are specified as the overlap between CSB being "L" and $/WR$ and $/RD$ being at the "L" level. CSB and $/WR$ (or $/RD$) cannot act at the same time and CSB should be 100ns wider than $/WR$ (or $/RD$).

9.2. System Bus Timing for 8080 Series MPU



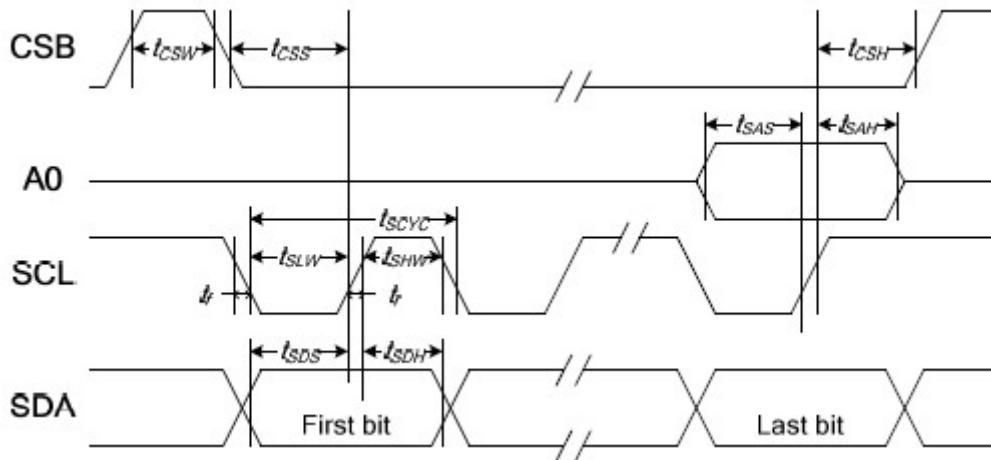
VDD1 = 3.3V~5.0V, Ta = 25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW8		10	—	ns
Address hold time		tAH8		0	—	
System cycle time	/WR	tCYC8		1100	—	
/WR L pulse width (WRITE)		tCCLW		500	—	
/WR H pulse width (WRITE)		tCCHW		500	—	
/RD L pulse width (READ)		/RD	tCCLR		950	
/RD H pulse width (READ)	tCCHR			500	—	
CSB setup time	CSB	tCSS8		100	—	
CSB hold time		tCSH8		100	—	
WRITE Data setup time	D[7:0]	tDS8		200	—	
WRITE Data hold time		tDH8		50	—	
READ access time		tACC8	CL = 100 pF	—	950	
READ Output disable time		tOH8	CL = 100 pF	5	200	

Note:

- The input signal rise time and fall time (t_r , t_f) is specified at 15 ns or less. When the system cycle time is extremely fast, $(t_r + t_f) \leq (t_{CYC8} - t_{CCLW} - t_{CCHW})$ for $(t_r + t_f) \leq (t_{CYC8} - t_{CCLR} - t_{CCHR})$ are specified.
- All timing is specified using 20% and 80% of VDD1 as the reference.
- tCCLW and tCCLR are specified as the overlap between CSB being "L" and /WR and /RD being at the "L" level. CSB and /WR (or /RD) cannot act at the same time and CSB should be 100ns wider than /WR (or /RD).

9.3. System Bus Timing for 4-line Serial MPU



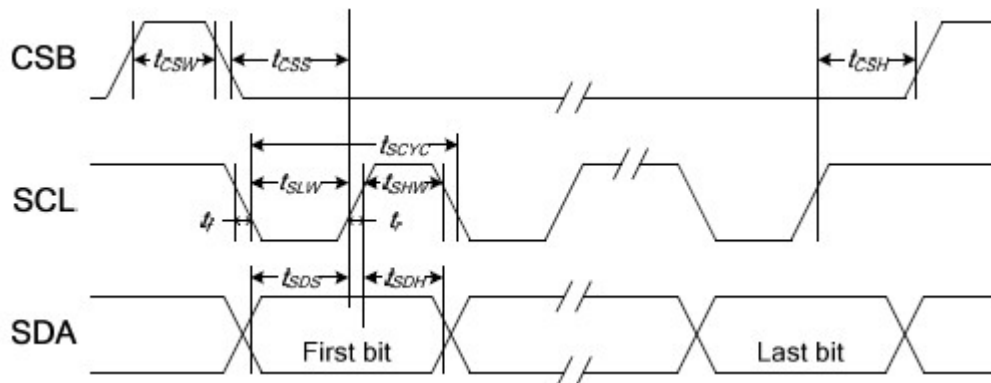
VDD1 = 3.3V~5.0V , Ta = 25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period	SCL	tSCYC		250	—	ns
SCL "H" pulse width		tSHW		100	—	
SCL "L" pulse width		tSLW		100	—	
Address setup time	A0	tSAS		150	—	
Address hold time		tSAH		150	—	
Data setup time	SDA	tSDS		100	—	
Data hold time		tSDH		100	—	
CSB-SCL time	CSB	tCSS		150	—	
CSB-SCL time		tCSH		150	—	
CSB "H" pulse width		tCSW		25	—	

Note:

1. The input signal rise and fall time (t_r , t_f) are specified at 15 ns or less.
2. All timing is specified using 20% and 80% of VDD1 as the standard.

9.4. System Bus Timing for 3-line Serial MPU



VSS=0V, VDD1 = VDDA=2.7 ~ 5.5V, Ta = -40 ~ 105°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial Clock Period	SCL	tSCYC		250	—	ns
SCL "H" pulse width		tSHW		100	—	
SCL "L" pulse width		tSLW		100	—	
Data setup time	SDA	tSDS		100	—	
Data hold time		tSDH		100	—	
CSB-SCL time	CSB	tCSS		150	—	
CSB-SCL time		tCSH		150	—	
CSB "H" pulse width		tCSW		10	—	

Note:

1. The input signal rise and fall time (t_r , t_f) are specified at 15 ns or less.
2. All timing is specified using 20% and 80% of VDD1 as the standard.

10. Quality Assurance

10.1 Purpose

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

10.2 Standard for Quality Test

10.2.1 Sampling Plan:

GB2828.1-2012

Single sampling, normal inspection.

10.2.2 Sampling Criteria:

Visual inspection: AQL 1.5%

Electrical functional: AQL 0.65%.

10.2.3 Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.3 Nonconforming Analysis & Disposition

10.3.1 Nonconforming analysis:

10.3.1.1 Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2 After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3 If cannot finish the analysis on time, customer will be notified with the progress status.

10.3.2 Disposition of nonconforming:

10.3.2.1 Non-conforming product over PPM level will be replaced.

10.3.2.2 The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

10.4 Agreement Items

Shall negotiate with customer if the following situation occurs:

10.4.1 There is any discrepancy in standard of quality assurance.

10.4.2 Additional requirement to be added in product specification.

10.4.3 Any other special problem.

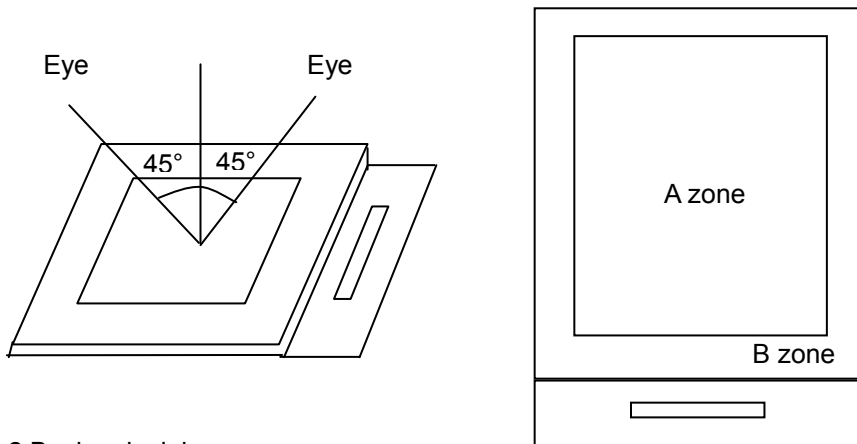
10.5 Standard of the Product Visual Inspection

10.5.1 Appearance inspection:

10.5.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area,

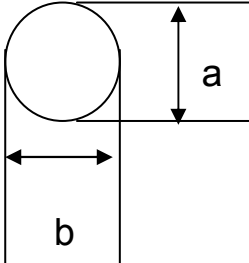


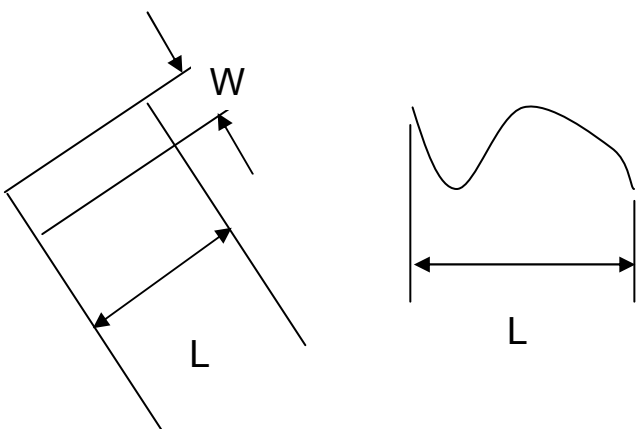
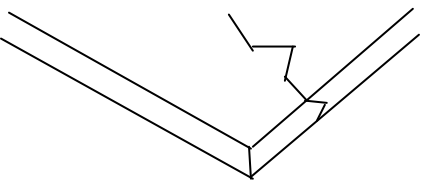
10.5.2 Basic principle:

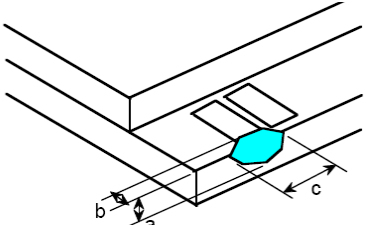
10.5.2.1 A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

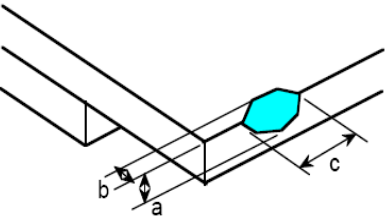
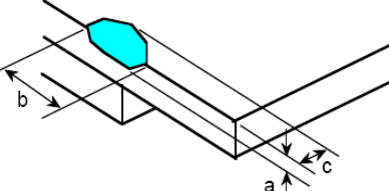
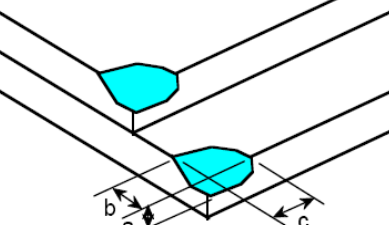
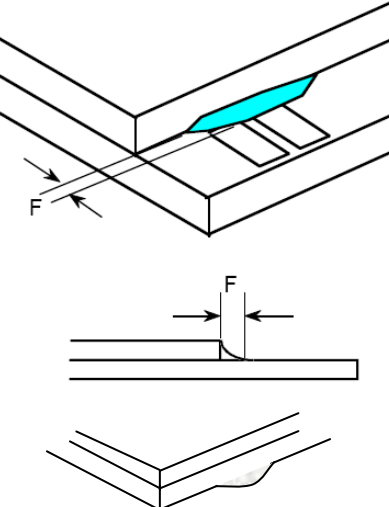
10.5.2.2 New item must be added on time when it is necessary.

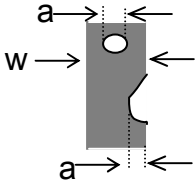
10.6 Inspection Specification

No.	Item	Criteria (Unit: mm)																
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 <table border="1" data-bbox="909 996 1412 1209"> <thead> <tr> <th>Size</th> <th>Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.20$</td> <td></td> <td>Ignore</td> </tr> <tr> <td>$0.20 < \phi \leq 0.50$</td> <td></td> <td>$N \leq 3$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td></td> <td>0</td> </tr> </tbody> </table> <p>$\phi = (a + b) / 2$ Distance between 2 defects should more than 5mm apart.</p>	Size	Area	Acc. Qty	$\phi \leq 0.20$		Ignore	$0.20 < \phi \leq 0.50$		$N \leq 3$	$0.50 < \phi$		0				
Size	Area	Acc. Qty																
$\phi \leq 0.20$		Ignore																
$0.20 < \phi \leq 0.50$		$N \leq 3$																
$0.50 < \phi$		0																
02	Electrical Defect (Minor defect)	<table border="1" data-bbox="574 1406 1404 1668"> <thead> <tr> <th rowspan="2">Bright dot</th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td>$N \leq 2$</td> <td>$N \leq 2$</td> </tr> <tr> <th>Dark dot</th> <td>$N \leq 4$</td> <td>$N \leq 4$</td> </tr> <tr> <th>Total dot</th> <td>$N \leq 4$</td> <td>$N \leq 4$</td> </tr> <tr> <th>Mura</th> <td colspan="2">Not visible through 5% ND filter.</td> <th>Note 2</th> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>	Bright dot	Display Area	Total	Note1	$N \leq 2$	$N \leq 2$	Dark dot	$N \leq 4$	$N \leq 4$	Total dot	$N \leq 4$	$N \leq 4$	Mura	Not visible through 5% ND filter.		Note 2
Bright dot	Display Area	Total		Note1														
	$N \leq 2$	$N \leq 2$																
Dark dot	$N \leq 4$	$N \leq 4$																
Total dot	$N \leq 4$	$N \leq 4$																
Mura	Not visible through 5% ND filter.		Note 2															

<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1" data-bbox="635 745 1262 1010"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td>$W \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.1 < W \leq 0.2$</td> <td>3</td> </tr> <tr> <td>$L > 2.5$</td> <td>$0.2 < W$</td> <td>0</td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.1$	Ignore	$L \leq 2.5$	$0.1 < W \leq 0.2$	3	$L > 2.5$	$0.2 < W$	0	Total		3
Length	Width	Acc. Qty															
/	$W \leq 0.1$	Ignore															
$L \leq 2.5$	$0.1 < W \leq 0.2$	3															
$L > 2.5$	$0.2 < W$	0															
Total		3															
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>															

<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="869 1724 1340 1899"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>3</td> </tr> <tr> <td colspan="2">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty									
$c > 3.0, b < 1.0$	1									
$c < 3.0, b < 1.0$	3									
$a < \text{Glass Thickness}$										

<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c > 3.0, b < 1.0$</td> <td>1</td> </tr> <tr> <td>$c < 3.0, b < 1.0$</td> <td>2</td> </tr> <tr> <td>$c < 3.0, b < 0.5$</td> <td>4</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	$a < \text{Glass Thickness}$	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
$a < \text{Glass Thickness}$												
<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$c < 3.0, b < 3.0$</td> <td>Ignore</td> </tr> <tr> <td colspan="2" style="text-align: center;">$a < \text{Glass Thickness}$</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	$a < \text{Glass Thickness}$					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
$a < \text{Glass Thickness}$												
<p>09</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$F < 1.0$</td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											

10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width $a < w/3$. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>								
11	Bubble on Polarizer (Minor defect)	<table border="1" data-bbox="743 577 1214 748"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.30$</td> <td>Ignore</td> </tr> <tr> <td>$0.30 < \phi \leq 0.50$</td> <td>$N \leq 2$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td>$N = 0$</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\phi \leq 0.30$	Ignore	$0.30 < \phi \leq 0.50$	$N \leq 2$	$0.50 < \phi$	$N = 0$
Diameter	Acc. Qty									
$\phi \leq 0.30$	Ignore									
$0.30 < \phi \leq 0.50$	$N \leq 2$									
$0.50 < \phi$	$N = 0$									
12	Dent on Polarizer (Minor defect)	<table border="1" data-bbox="743 819 1214 990"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.25$</td> <td>Ignore</td> </tr> <tr> <td>$0.25 < \phi \leq 0.50$</td> <td>$N \leq 4$</td> </tr> <tr> <td>$0.50 < \phi$</td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\phi \leq 0.25$	Ignore	$0.25 < \phi \leq 0.50$	$N \leq 4$	$0.50 < \phi$	None
Diameter	Acc. Qty									
$\phi \leq 0.25$	Ignore									
$0.25 < \phi \leq 0.50$	$N \leq 4$									
$0.50 < \phi$	None									
13	Bezel	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>								
14	Touch Panel	<p>D: Diameter W: width L: length 14.1 Spot: $D < 0.25$ is acceptable $0.25 \leq D \leq 0.4$ 2dots are acceptable and the distance between defects should more than 10 mm. $D > 0.4$ is unacceptable 14.2 Dent: $D > 0.40$ is unacceptable 14.3 Scratch: $W \leq 0.03$, $L \leq 10$ is acceptable, $0.03 < W \leq 0.10$, $L \leq 10$ is acceptable Distance between 2 defects should more than 10 mm. $W > 0.10$ is unacceptable.</p>								
15	PCB	<p>15.1 No distortion or contamination on PCB terminals. 15.2 All components on PCB must same as documented on the BOM/component layout. 15.3 Follow IPC-A-600F.</p>								
16	Soldering	Follow IPC-A-610C standard								

17	Electrical Defect (Major defect)	<p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment, 17.2 Abnormal Display. 17.3 No function or no display. 17.4 Current exceeds product specifications. 17.5 LCD viewing angle defect. 17.6 No Backlight. 17.7 Dark Backlight. 17.8 Touch Panel no function.</p>
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Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

10.7 Classification of Defects

10.7.1 Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

10.7.2 Two minor defects are equal to one major in lot sampling inspection.

10.8 Identification/marketing criteria

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

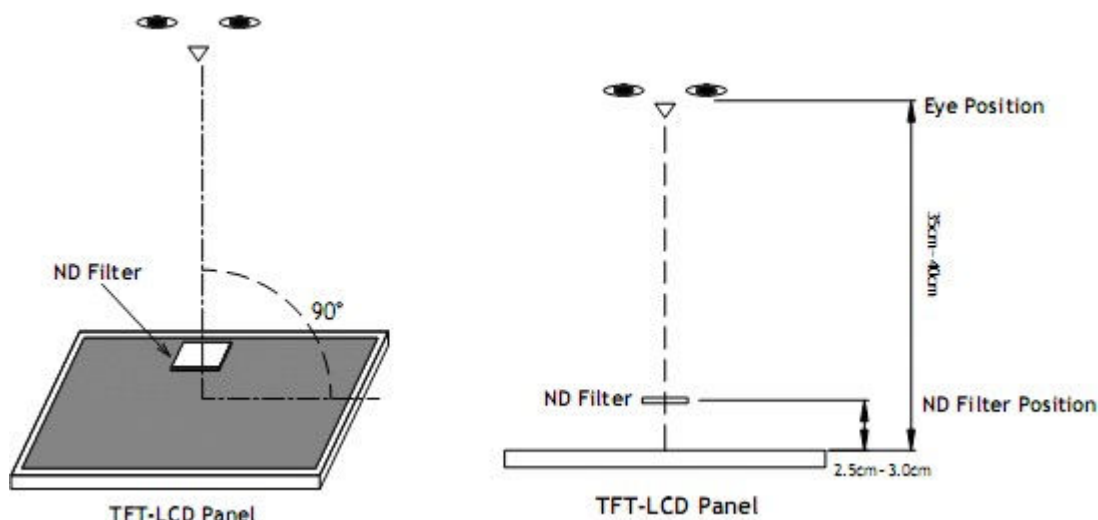
10.9 Packaging

10.9.1 There should be no damage of the outside carton box, each packaging box should have one identical label.

10.9.2 Modules inside package box should have compliant mark.

10.9.3 All direct package materials shall offer ESD protection

Note1: Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is $350\text{mm} \pm 50\text{mm}$.

Note2: Mura on display which appears darker / brighter against background brightness on parts of display area.

11. Reliability Specification

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	70°C, 96Hrs	2	GB/T2423.2-2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1-2008
3	High Humidity	50°C, 90%RH, 96Hrs	2	GB/T2423.3-2006
4	High Temperature Storage	80°C, 96Hrs	2	GB/T2423.2-2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1-2008
6	Thermal Cycling Test	-20°C, 60min~70°C, 60min, 20 cycles.	2	GB/T2423.22-2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.14-2009
8	Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times Contact: ±2KV 150pF/330Ω 5 times	2	GB/T17626.2-2006
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8-1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value

12. Precautions and Warranty

12.1. Safety

12.1.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

12.1.2 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2. Handling

12.2.1 Reverse and use within ratings in order to keep performance and prevent damage.

12.2.2 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3. Storage

12.3.1. Do not store the LCD module beyond the specified temperature ranges.

12.3.2. Strong light exposure causes degradation of polarizer and color filter

12.4. Metal Pin (Apply to Products with Metal Pins)

12.4.1. Pins of LCD and Backlight

12.4.1.1 Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2 Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

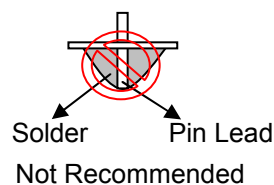
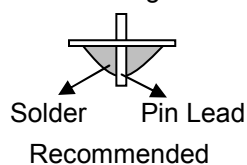
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

12.4.1.3 Solder Wetting



12.4.2. Pins of EL

12.4.2.1 Solder tip can touch and press on the tip of EL leads during soldering.

12.4.2.2 No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3 Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290℃

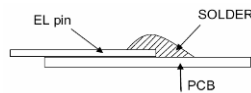
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

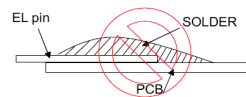
12.4.2.4 No horizontal press on the EL leads during soldering.

12.4.2.5 180° bend EL leads three times is not allowed.

12.4.2.6 Solder Wetting

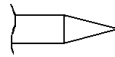


Recommended

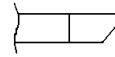


Not Recommended

12.4.2.7 The type of the solder iron:

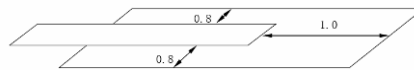


Recommended



Not Recommended

12.4.2.8 Solder Pad



12.5.Operation

- 12.5.1. Do not drive LCD with DC voltage
- 12.5.2. Response time will increase below lower temperature
- 12.5.3. Display may change color with different temperature
- 12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear "fractured".
- 12.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 12.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 12.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 12.5.8. Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it may develop image sticking due to the TFT structure.

12.6.Static Electricity

- 12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 12.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7.Limited Warranty

- 12.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 12.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.
- 12.7.3. After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Packaging

TBD

14. Outline Drawing

